

FIG. 2

0935051-0109

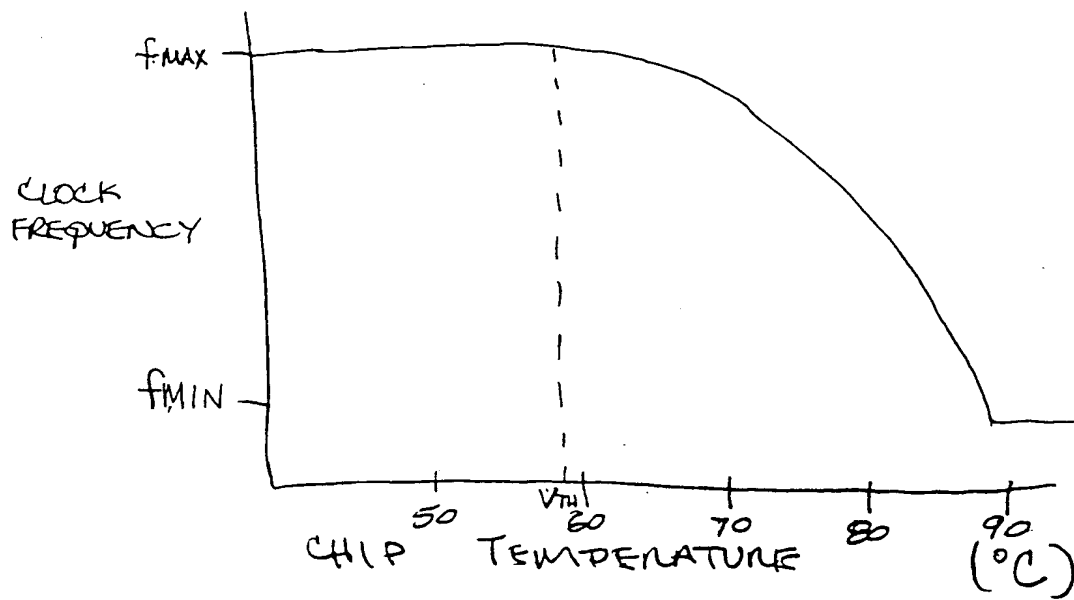


FIG. 2

0931051-071099

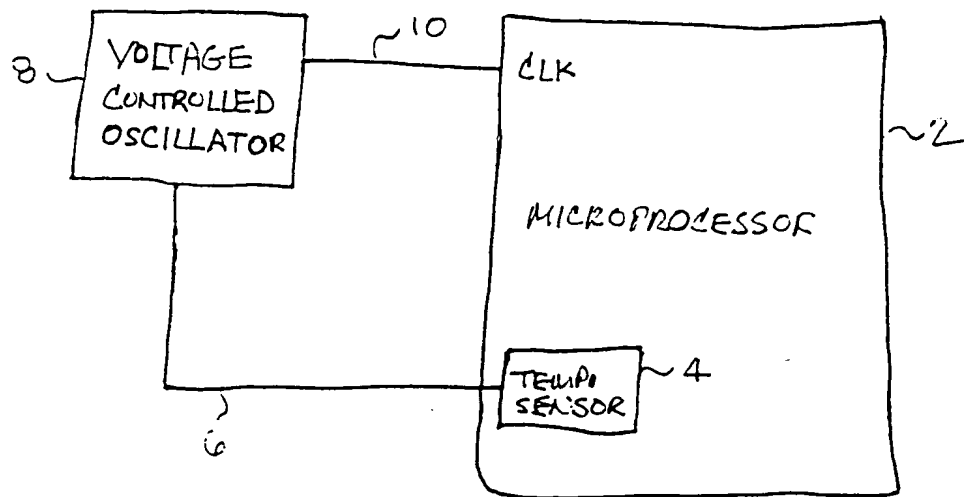


FIG. 1

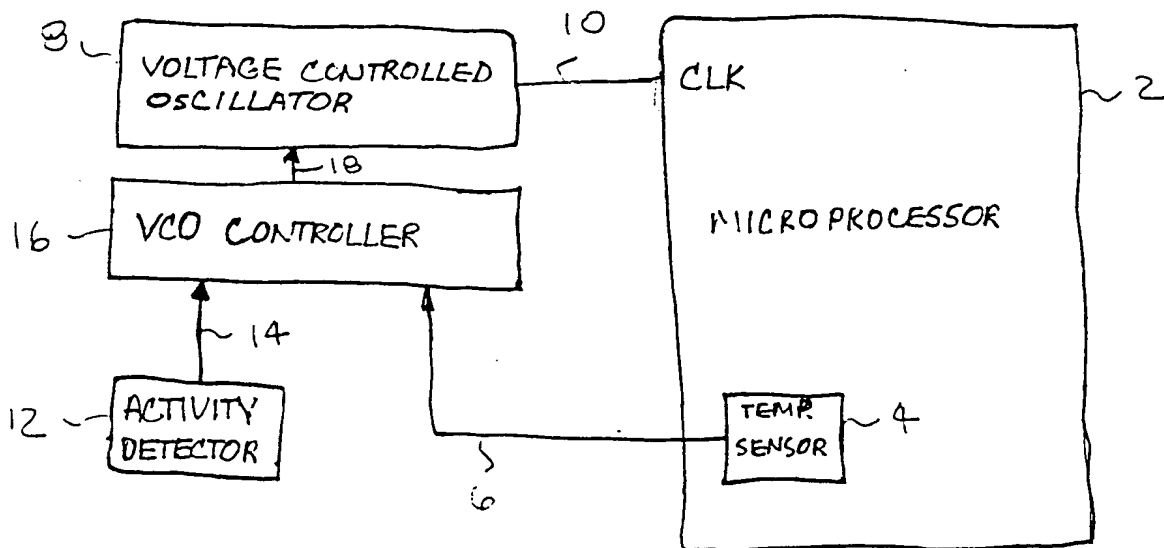


FIG. 3

FIG. 4

FIG. 5

Block diagram of a microprocessor system. An oscillator (22) is connected to a divider (24) inside a microprocessor (20). The divider (24) outputs to a 3-bit counter (26). The counter (26) outputs to a 32-bit counter (32), which is labeled CLK. The microprocessor (20) also contains a temp. sensor (4) connected to a 34-bit counter, and an activity detector (12) connected to a 36-bit counter. The microprocessor (20) is shown as a dashed box.

660420 "T50T560

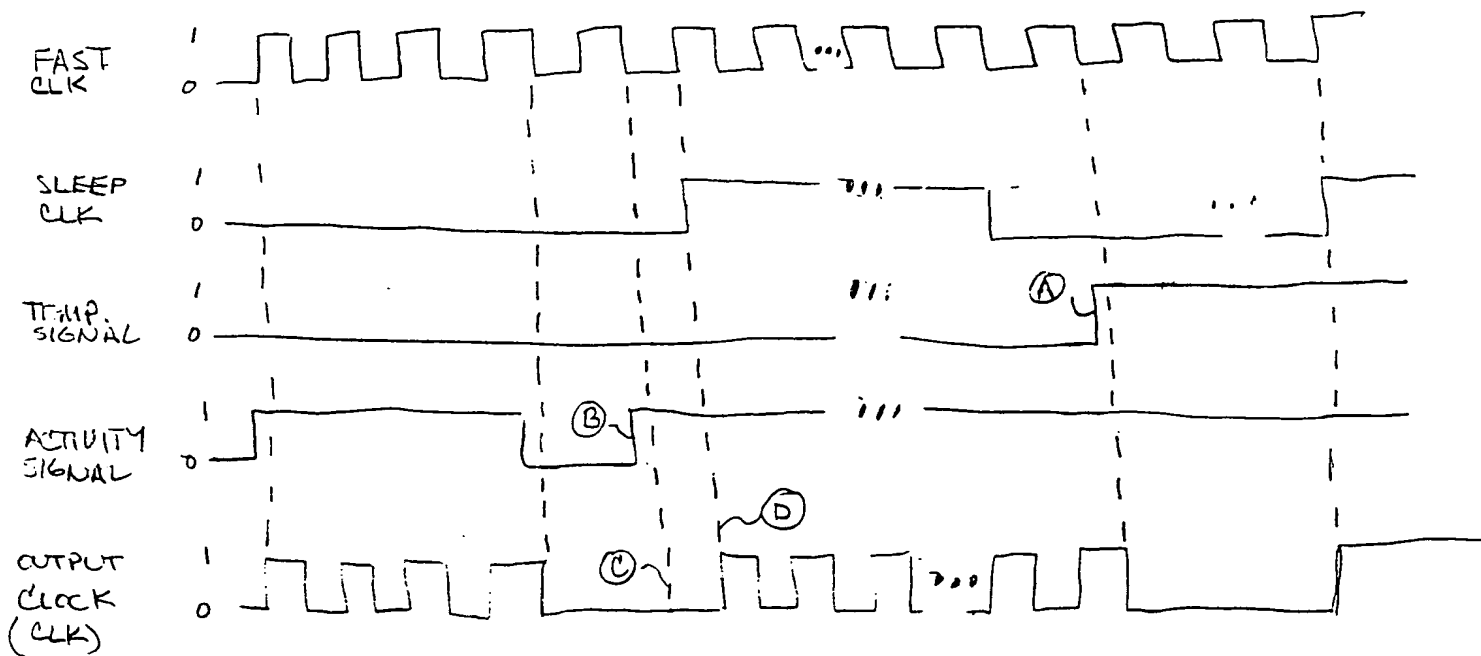


FIG. 6

660720 1504560

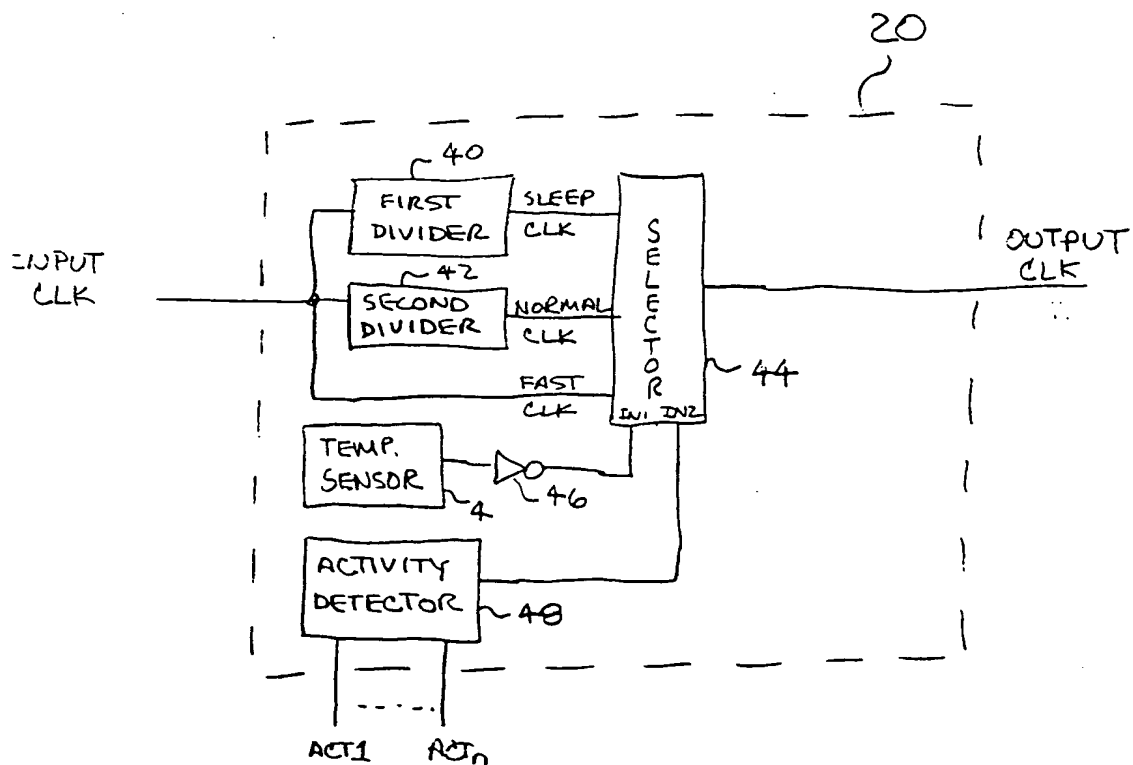


FIG. 7

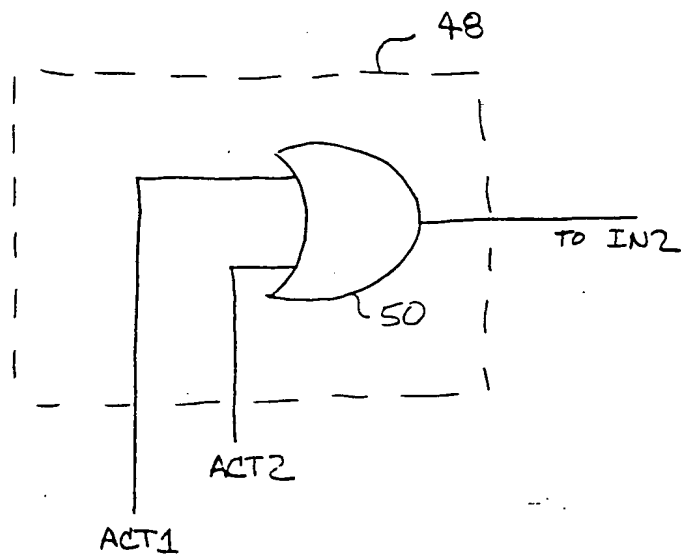


FIG. 8

660720 7507560

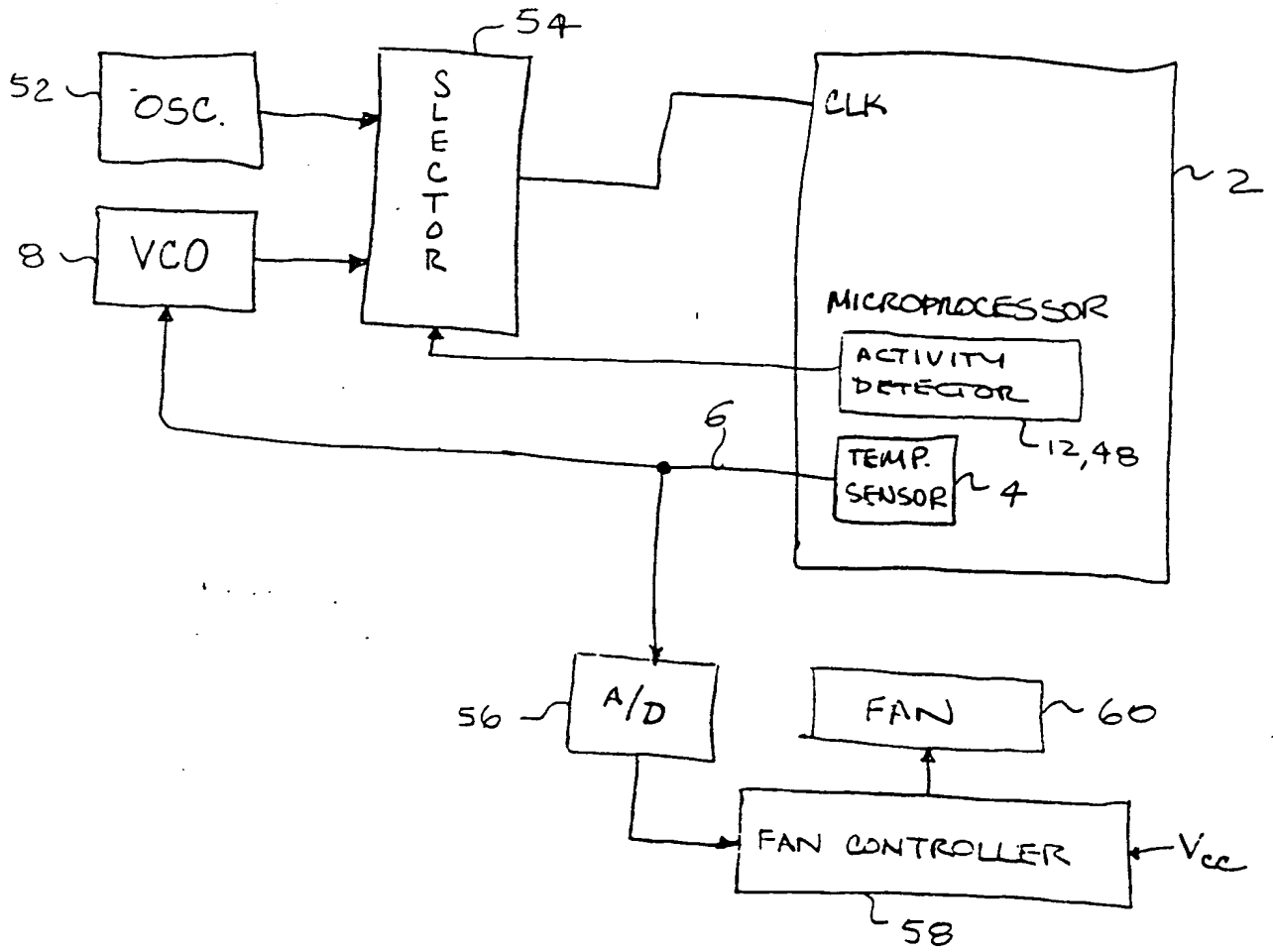


FIG. 9

680720 TEST 360

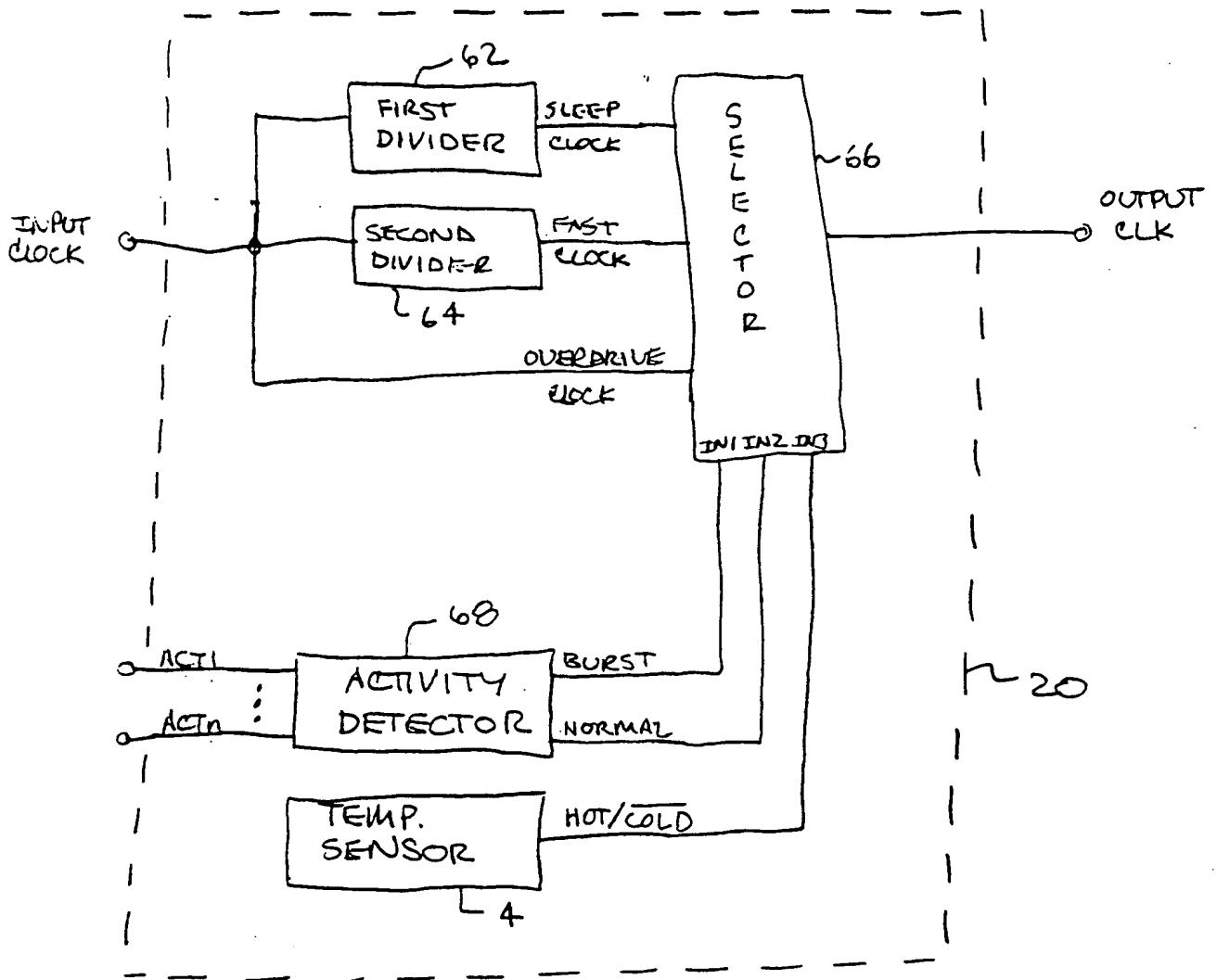


FIG. 10